

Preface

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Received: 4 May 2009 / Accepted: 14 May 2009 / Published online: 3 July 2009
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This special issue of Formal Methods in System Design is devoted to extended versions of selected papers from the 20th International Conference on Computer Aided Verification (CAV) held in Princeton, New Jersey, USA, from July 7–14, 2008. The papers selected here range over verification of hardware, software, and hybrid systems, and were evaluated to be among the best by the program committee.

The first CAV Award was presented at CAV 2008. This special issue also includes the citation for the CAV Award, and a description of the full process governing the administration of the CAV Award.

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